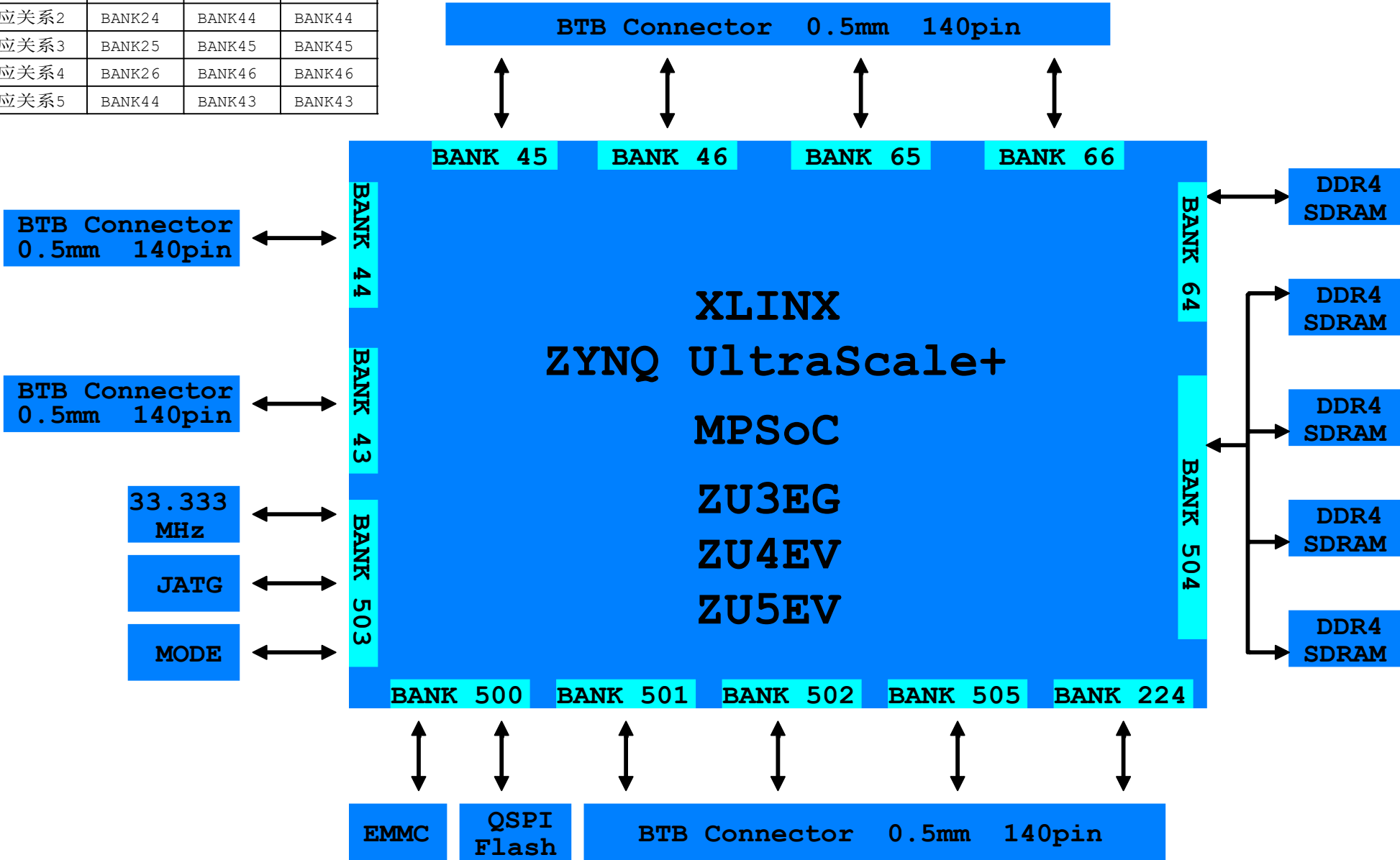


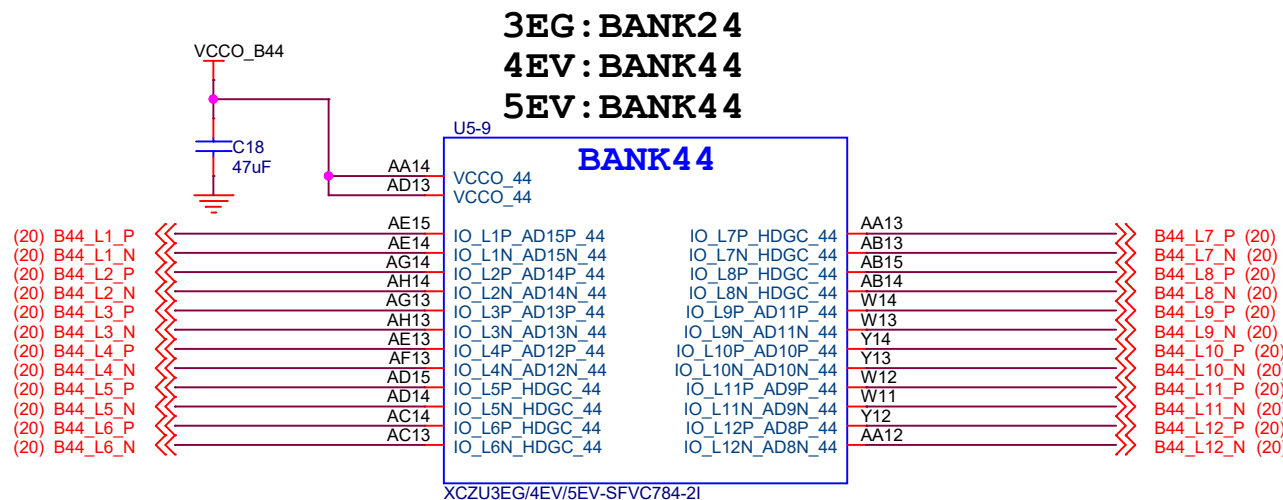
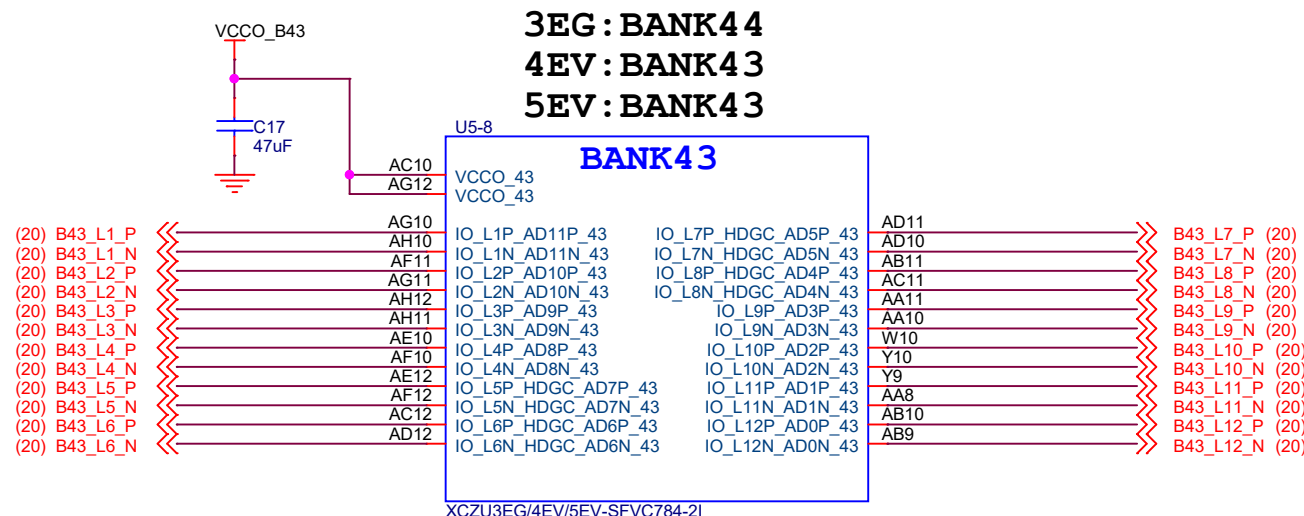
ZU4EV和ZU5EV的BANK号完全一致
ZU3EG有5个BANK号与其他两个有差异

对应关系\型号	ZU3EG	ZU4EV	ZU5EV
对应关系1	NC (空脚)	BANK224	BANK224
对应关系2	BANK24	BANK44	BANK44
对应关系3	BANK25	BANK45	BANK45
对应关系4	BANK26	BANK46	BANK46
对应关系5	BANK44	BANK43	BANK43



⁵
BANK43和BANK44为HD BANK, 应用于低速I/O的场景, 最高速率限制在250M以内, 最高电压也是支持到3.3V

1

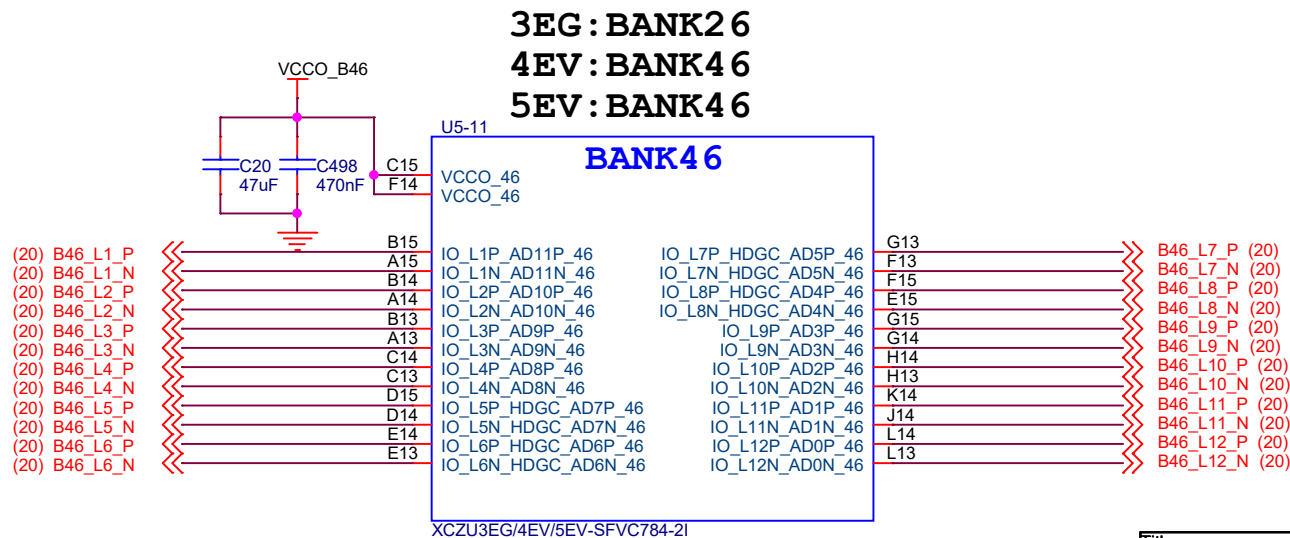
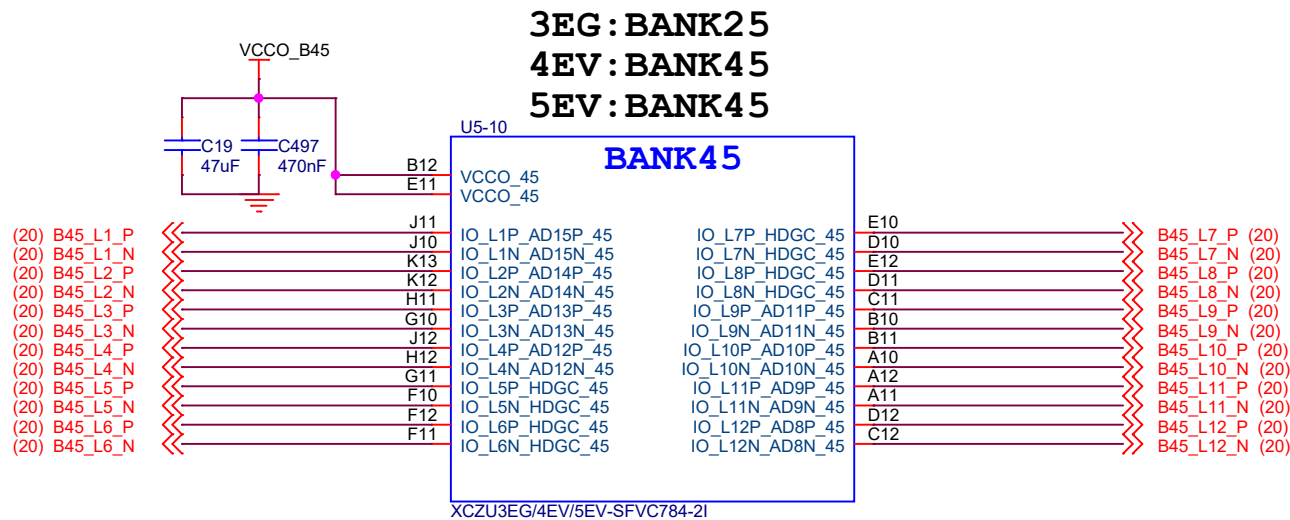


Title: MPSOC P5 CORE BTB 1V1 20251009	
Author: ALIENTEK	Date: Thursday, October 09, 2025
Version: V1.1	File: 02_BANK43_44
Size: A4	Sheet 2 of 21



正点原子

BANK45和BANK46为HD BANK，应用于低速I/O的场景，最高速率限制在250M以内，最高电压也是支持到3.3V



Title: MPSOC P5 CORE BTB 1V1 20251009	
Author: ALIENTEK	Date: Thursday, October 09, 2025
Version: V1.1	File: 03 BANK 45 46
Size: A4	Sheet 3 of 21



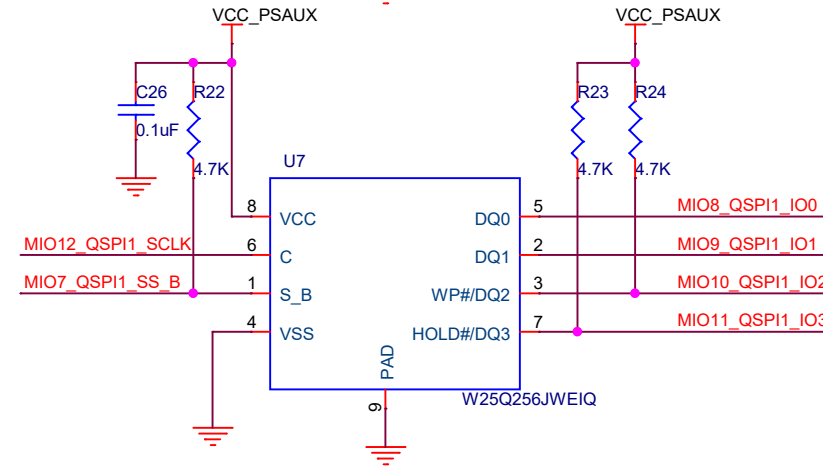
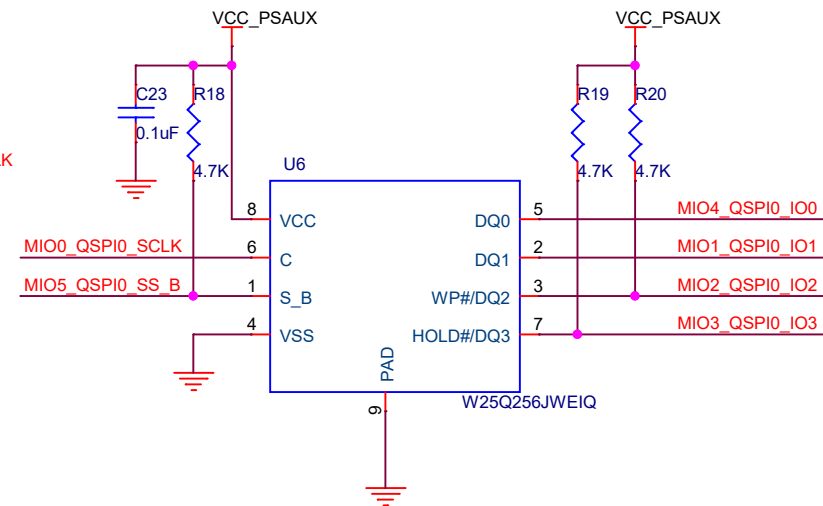
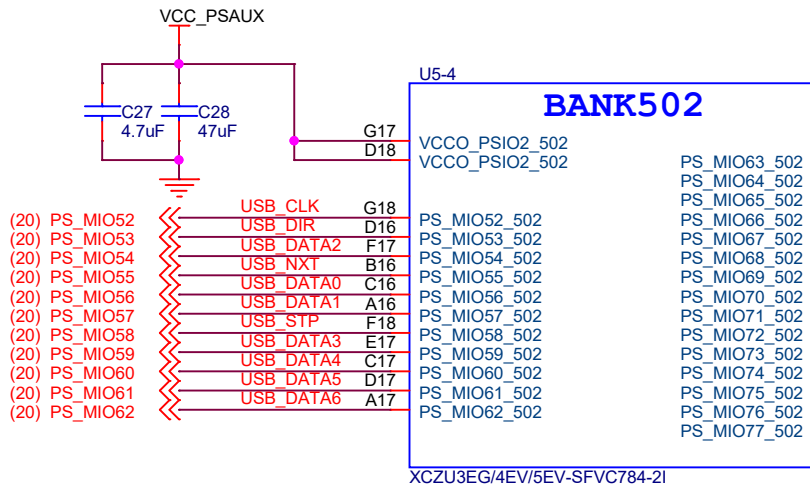
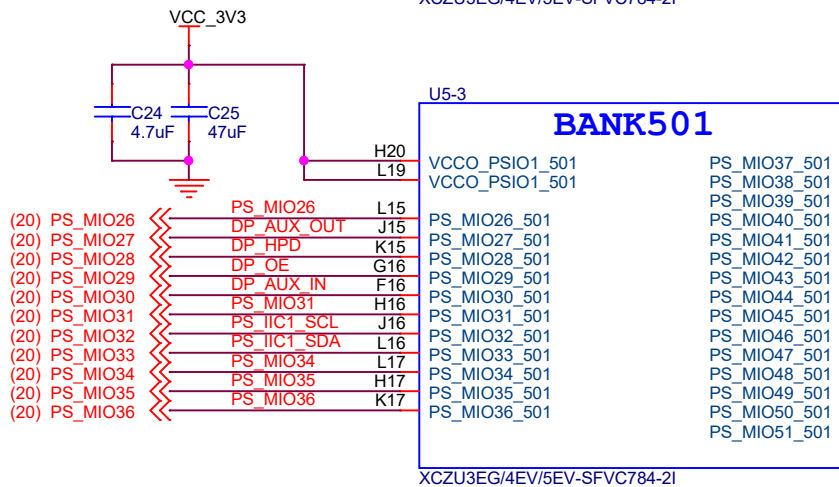
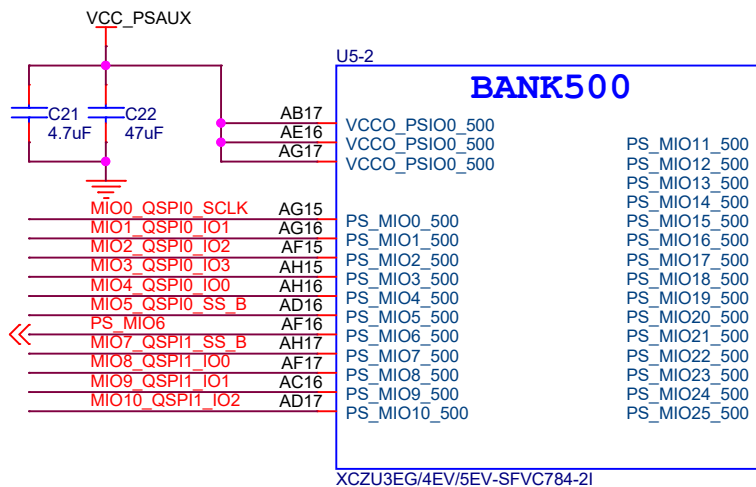
D

(20) PS_MIO6 <<

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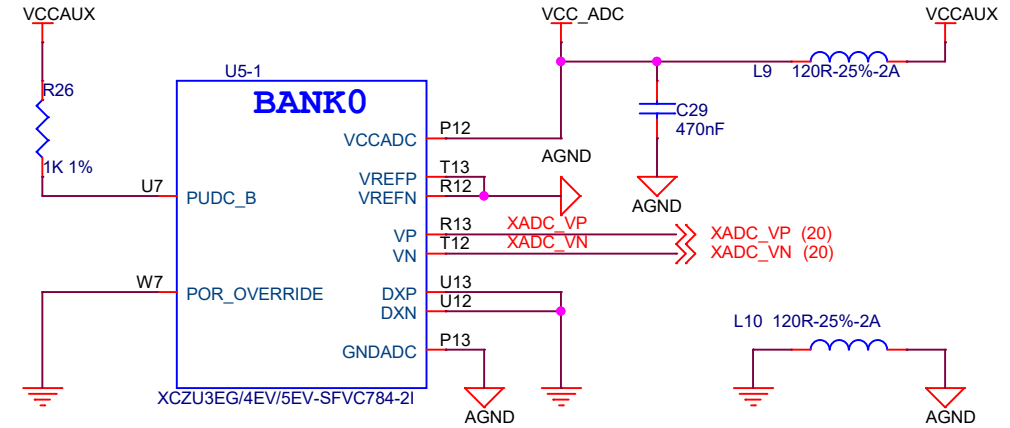


Title:	MPSOC P5 CORE BTB 1V1 20251009	
Author:	ALIENTEK	Date: Thursday, October 09, 2025
Version:	V1.1	File: 04 BANK 500 501 502 QSPI
Size: A4	Sheet 4	of 21

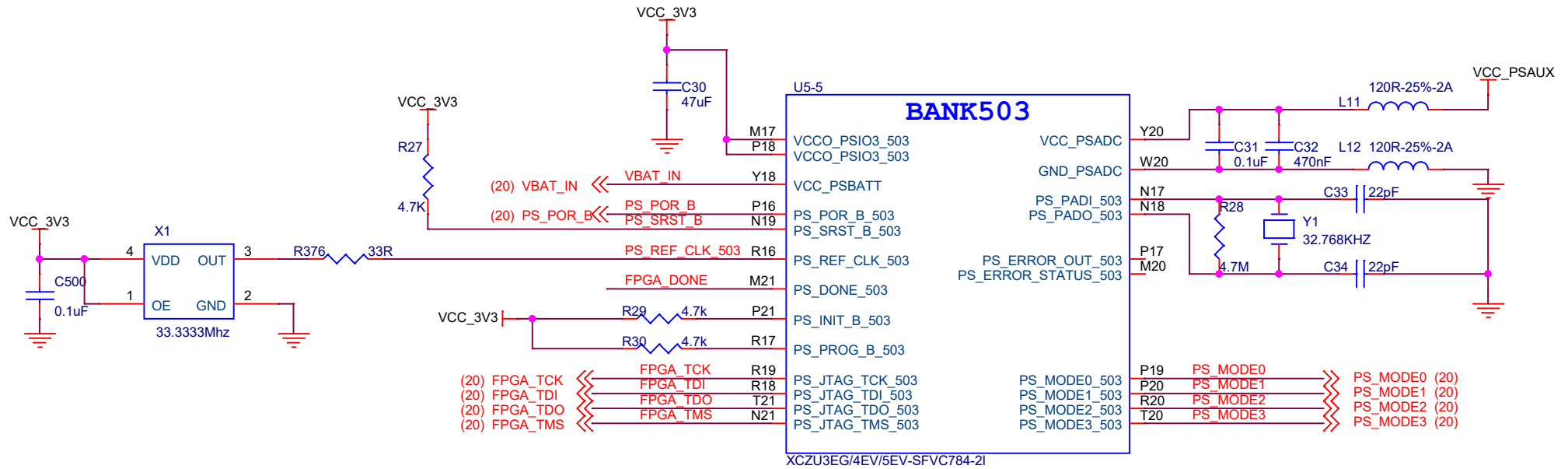


D

POWER LED



C



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A

Title: MPSOC P5 CORE BTB 1V1 20251009			
Author: ALIENTEK		Date: Thursday, October 09, 2025	
Version: V1.1		File: 05 BANK 503	
Size: A4	Sheet	5 of	21



D

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U5-7

BANK504

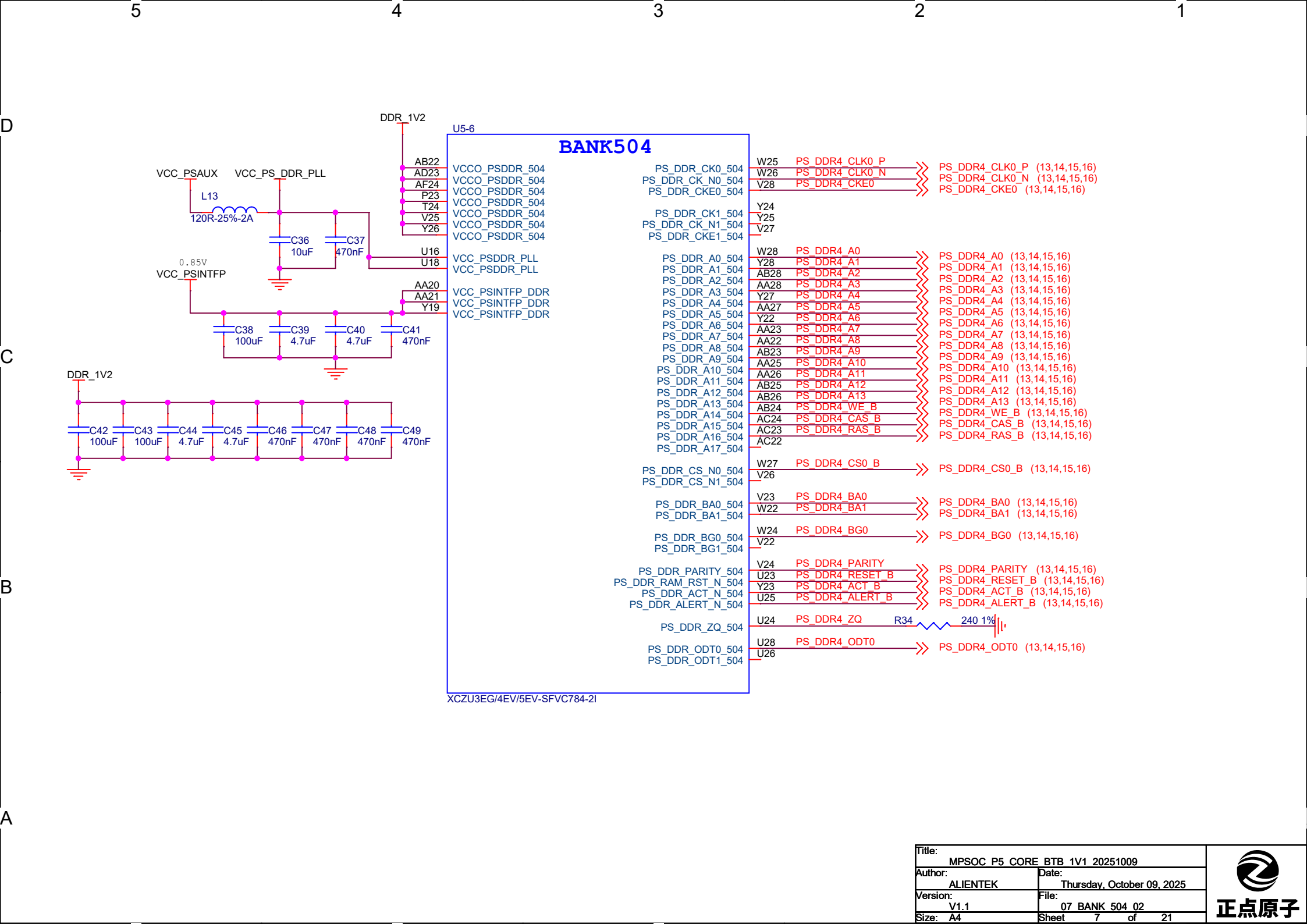
(13) PS_DDR4_DQ0	PS_DDR4_DQ0	AD21	PS_DDR_DQ0_504	PS_DDR_DQ32_504	T22 PS_DDR4_DQ32	PS_DDR4_DQ32 (15)
(13) PS_DDR4_DQ1	PS_DDR4_DQ1	AE20	PS_DDR_DQ1_504	PS_DDR_DQ33_504	R22 PS_DDR4_DQ33	PS_DDR4_DQ33 (15)
(13) PS_DDR4_DQ2	PS_DDR4_DQ2	AD20	PS_DDR_DQ2_504	PS_DDR_DQ34_504	P22 PS_DDR4_DQ34	PS_DDR4_DQ34 (15)
(13) PS_DDR4_DQ3	PS_DDR4_DQ3	AF20	PS_DDR_DQ3_504	PS_DDR_DQ35_504	N22 PS_DDR4_DQ35	PS_DDR4_DQ35 (15)
(13) PS_DDR4_DQ4	PS_DDR4_DQ4	AH21	PS_DDR_DQ4_504	PS_DDR_DQ36_504	T23 PS_DDR4_DQ36	PS_DDR4_DQ36 (15)
(13) PS_DDR4_DQ5	PS_DDR4_DQ5	AH20	PS_DDR_DQ5_504	PS_DDR_DQ37_504	P24 PS_DDR4_DQ37	PS_DDR4_DQ37 (15)
(13) PS_DDR4_DQ6	PS_DDR4_DQ6	AH19	PS_DDR_DQ6_504	PS_DDR_DQ38_504	R24 PS_DDR4_DQ38	PS_DDR4_DQ38 (15)
(13) PS_DDR4_DQ7	PS_DDR4_DQ7	AG19	PS_DDR_DQ7_504	PS_DDR_DQ39_504	N24 PS_DDR4_DQ39	PS_DDR4_DQ39 (15)
(13) PS_DDR4_DQ8	PS_DDR4_DQ8	AF22	PS_DDR_DQ8_504	PS_DDR_DQ40_504	H24 PS_DDR4_DQ40	PS_DDR4_DQ40 (16)
(13) PS_DDR4_DQ9	PS_DDR4_DQ9	AH22	PS_DDR_DQ9_504	PS_DDR_DQ41_504	J24 PS_DDR4_DQ41	PS_DDR4_DQ41 (16)
(13) PS_DDR4_DQ10	PS_DDR4_DQ10	AE22	PS_DDR_DQ10_504	PS_DDR_DQ42_504	M24 PS_DDR4_DQ42	PS_DDR4_DQ42 (16)
(13) PS_DDR4_DQ11	PS_DDR4_DQ11	AD22	PS_DDR_DQ11_504	PS_DDR_DQ43_504	K24 PS_DDR4_DQ43	PS_DDR4_DQ43 (16)
(13) PS_DDR4_DQ12	PS_DDR4_DQ12	AH23	PS_DDR_DQ12_504	PS_DDR_DQ44_504	J22 PS_DDR4_DQ44	PS_DDR4_DQ44 (16)
(13) PS_DDR4_DQ13	PS_DDR4_DQ13	AH24	PS_DDR_DQ13_504	PS_DDR_DQ45_504	H22 PS_DDR4_DQ45	PS_DDR4_DQ45 (16)
(13) PS_DDR4_DQ14	PS_DDR4_DQ14	AE24	PS_DDR_DQ14_504	PS_DDR_DQ46_504	K22 PS_DDR4_DQ46	PS_DDR4_DQ46 (16)
(13) PS_DDR4_DQ15	PS_DDR4_DQ15	AG24	PS_DDR_DQ15_504	PS_DDR_DQ47_504	L22 PS_DDR4_DQ47	PS_DDR4_DQ47 (16)
(14) PS_DDR4_DQ16	PS_DDR4_DQ16	AC26	PS_DDR_DQ16_504	PS_DDR_DQ48_504	M25 PS_DDR4_DQ48	PS_DDR4_DQ48 (15)
(14) PS_DDR4_DQ17	PS_DDR4_DQ17	AD26	PS_DDR_DQ17_504	PS_DDR_DQ49_504	M26 PS_DDR4_DQ49	PS_DDR4_DQ49 (15)
(14) PS_DDR4_DQ18	PS_DDR4_DQ18	AD25	PS_DDR_DQ18_504	PS_DDR_DQ50_504	L25 PS_DDR4_DQ50	PS_DDR4_DQ50 (15)
(14) PS_DDR4_DQ19	PS_DDR4_DQ19	AD24	PS_DDR_DQ19_504	PS_DDR_DQ51_504	L26 PS_DDR4_DQ51	PS_DDR4_DQ51 (15)
(14) PS_DDR4_DQ20	PS_DDR4_DQ20	AG26	PS_DDR_DQ20_504	PS_DDR_DQ52_504	K28 PS_DDR4_DQ52	PS_DDR4_DQ52 (15)
(14) PS_DDR4_DQ21	PS_DDR4_DQ21	AH25	PS_DDR_DQ21_504	PS_DDR_DQ53_504	L28 PS_DDR4_DQ53	PS_DDR4_DQ53 (15)
(14) PS_DDR4_DQ22	PS_DDR4_DQ22	AH26	PS_DDR_DQ22_504	PS_DDR_DQ54_504	M28 PS_DDR4_DQ54	PS_DDR4_DQ54 (15)
(14) PS_DDR4_DQ23	PS_DDR4_DQ23	AG25	PS_DDR_DQ23_504	PS_DDR_DQ55_504	N28 PS_DDR4_DQ55	PS_DDR4_DQ55 (15)
(14) PS_DDR4_DQ24	PS_DDR4_DQ24	AH27	PS_DDR_DQ24_504	PS_DDR_DQ56_504	J28 PS_DDR4_DQ56	PS_DDR4_DQ56 (16)
(14) PS_DDR4_DQ25	PS_DDR4_DQ25	AH28	PS_DDR_DQ25_504	PS_DDR_DQ57_504	K27 PS_DDR4_DQ57	PS_DDR4_DQ57 (16)
(14) PS_DDR4_DQ26	PS_DDR4_DQ26	AF28	PS_DDR_DQ26_504	PS_DDR_DQ58_504	H28 PS_DDR4_DQ58	PS_DDR4_DQ58 (16)
(14) PS_DDR4_DQ27	PS_DDR4_DQ27	AG28	PS_DDR_DQ27_504	PS_DDR_DQ59_504	H27 PS_DDR4_DQ59	PS_DDR4_DQ59 (16)
(14) PS_DDR4_DQ28	PS_DDR4_DQ28	AC27	PS_DDR_DQ28_504	PS_DDR_DQ60_504	G26 PS_DDR4_DQ60	PS_DDR4_DQ60 (16)
(14) PS_DDR4_DQ29	PS_DDR4_DQ29	AD27	PS_DDR_DQ29_504	PS_DDR_DQ61_504	G25 PS_DDR4_DQ61	PS_DDR4_DQ61 (16)
(14) PS_DDR4_DQ30	PS_DDR4_DQ30	AD28	PS_DDR_DQ30_504	PS_DDR_DQ62_504	K25 PS_DDR4_DQ62	PS_DDR4_DQ62 (16)
(14) PS_DDR4_DQ31	PS_DDR4_DQ31	AC28	PS_DDR_DQ31_504	PS_DDR_DQ63_504	J25 PS_DDR4_DQ63	PS_DDR4_DQ63 (16)
				PS_DDR_DQ64_504	T28	
				PS_DDR_DQ65_504	R28	
				PS_DDR_DQ66_504	P28	
				PS_DDR_DQ67_504	P27	
				PS_DDR_DQ68_504	P26	
				PS_DDR_DQ69_504	R25	
				PS_DDR_DQ70_504	P25	
				PS_DDR_DQ71_504	T25	
				PS_DDR_DM0_504	AG20 PS_DDR4_DM0	PS_DDR4_DM0 (13)
				PS_DDR_DM1_504	AE23 PS_DDR4_DM1	PS_DDR4_DM1 (13)
				PS_DDR_DM2_504	AE25 PS_DDR4_DM2	PS_DDR4_DM2 (14)
				PS_DDR_DM3_504	AE28 PS_DDR4_DM3	PS_DDR4_DM3 (14)
				PS_DDR_DM4_504	R23 PS_DDR4_DM4	PS_DDR4_DM4 (15)
				PS_DDR_DM5_504	H23 PS_DDR4_DM5	PS_DDR4_DM5 (16)
				PS_DDR_DM6_504	L27 PS_DDR4_DM6	PS_DDR4_DM6 (15)
				PS_DDR_DM7_504	H26 PS_DDR4_DM7	PS_DDR4_DM7 (16)
				PS_DDR_DM8_504	T26	

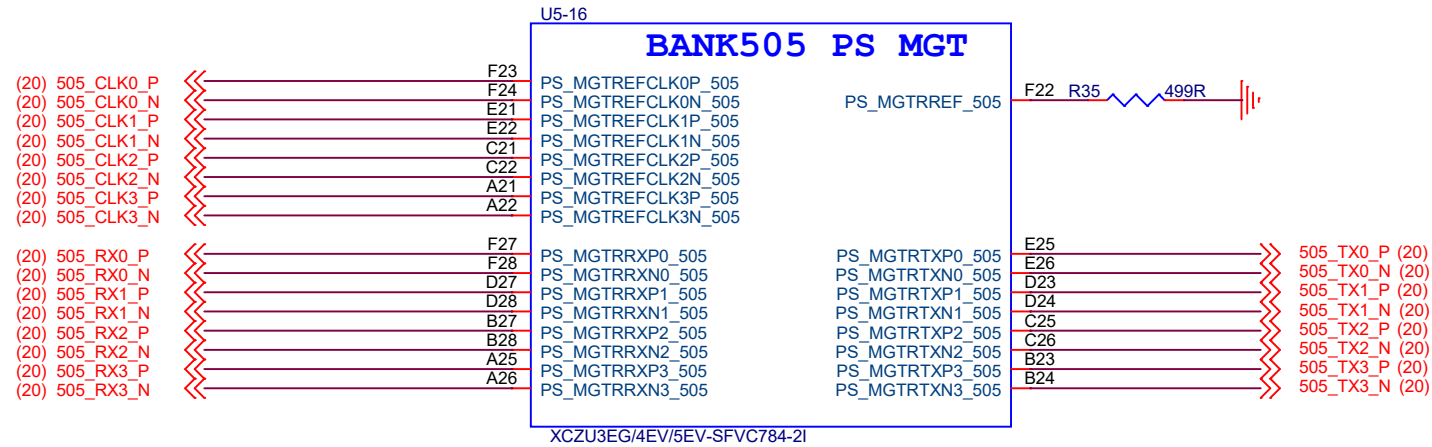
XCZU3EG/4EV/5EV-SFVC784-2I

Title: MPSOC P5 CORE BTB 1V1 20251009	
Author: ALIENTEK	Date: Thursday, October 09, 2025
Version: V1.1	File: 06 BANK 504 01
Size: A4	Sheet 6 of 21

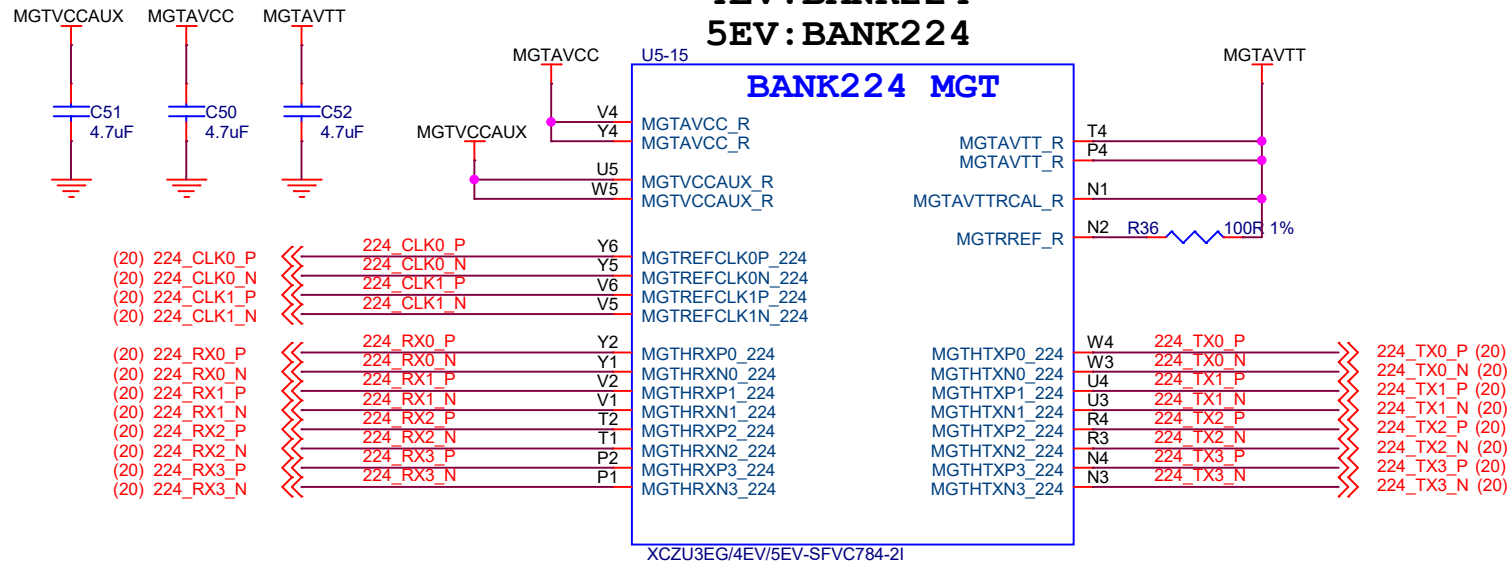


正点原子





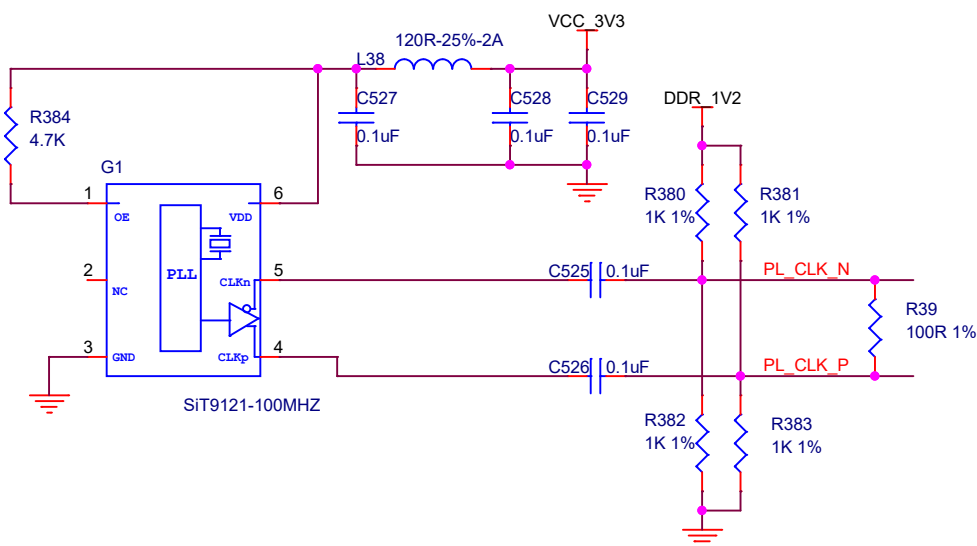
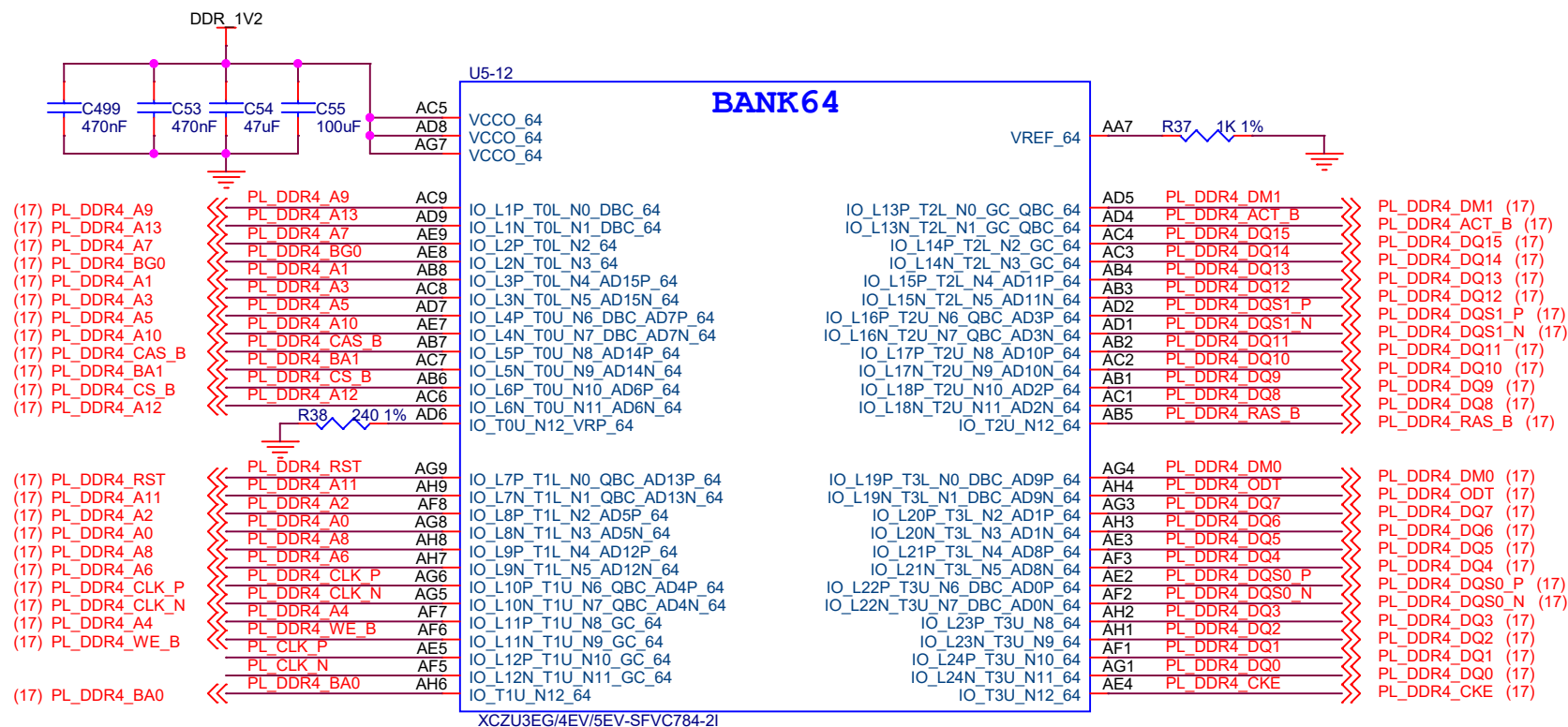
3EG:NC (空脚)
4EV: BANK224
5EV: BANK224



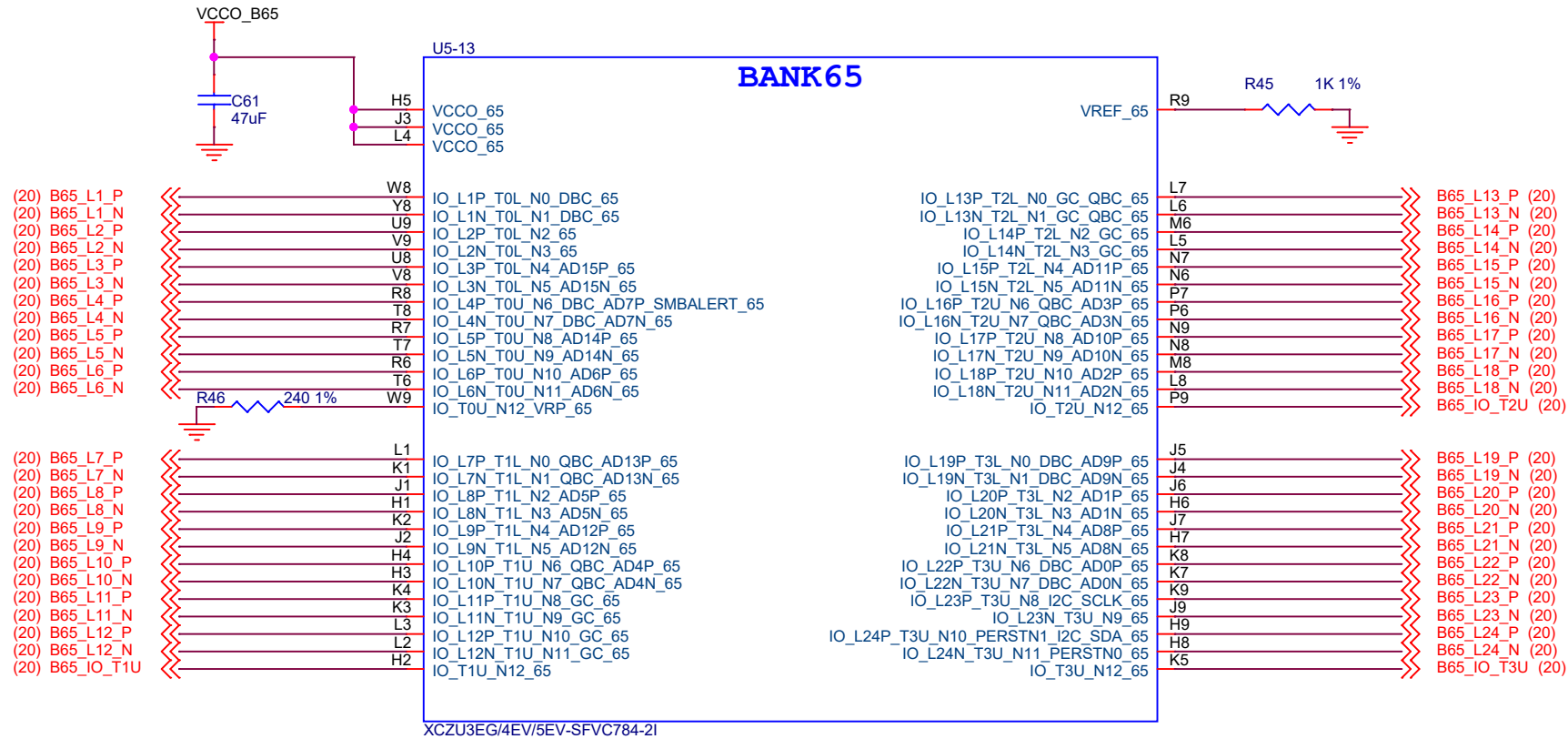
Title: MPSOC P5 CORE BTB 1V1 20251009	
Author: ALIENTEK	Date: Thursday, October 09, 2025
Version: V1.1	File: 08 BANK 505 224
Size: A4	Sheet 8 of 21



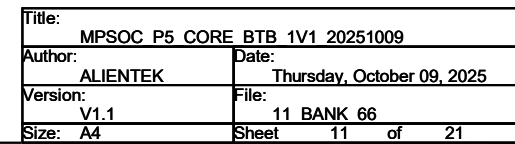
⁵**BANK64**为HP (High Performance) **BANK**, 应用于高性能也就是速度比较高的场景, ⁴由于速率比较高, **Bank**电压最高也只能到1.8v。

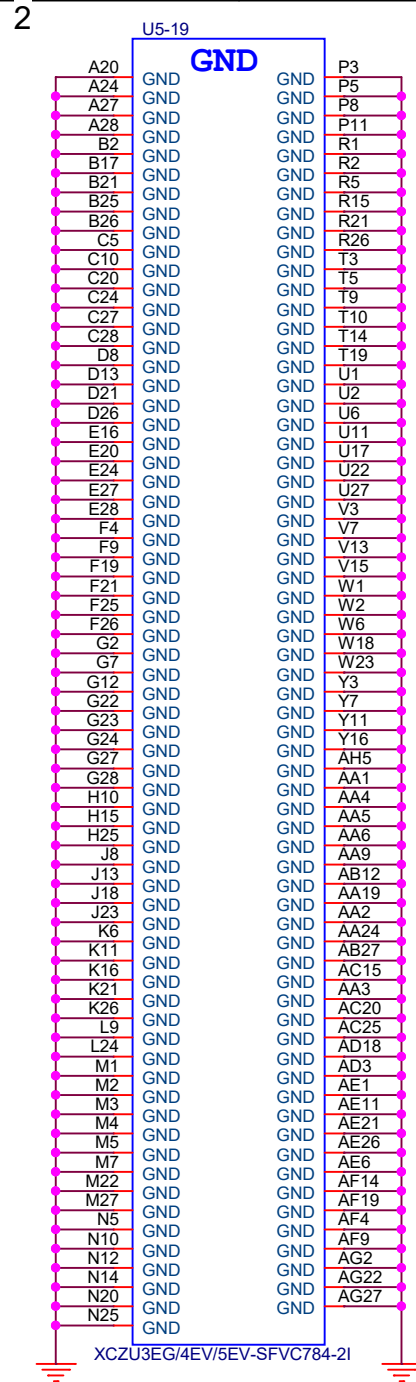
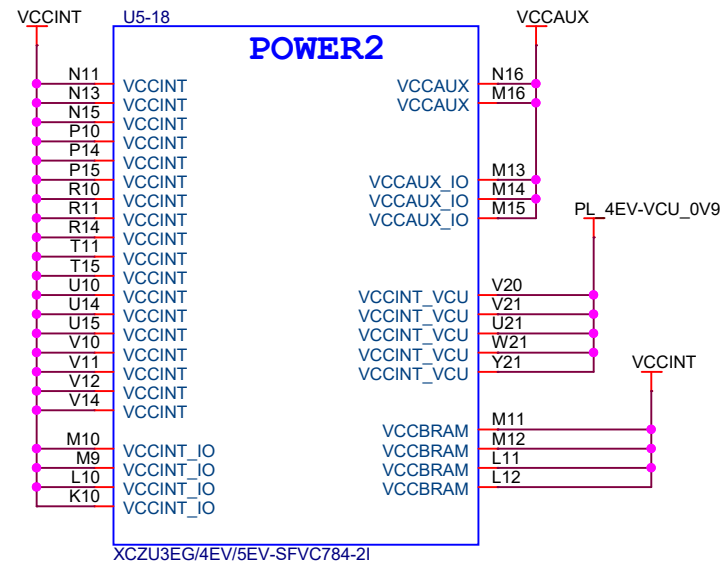
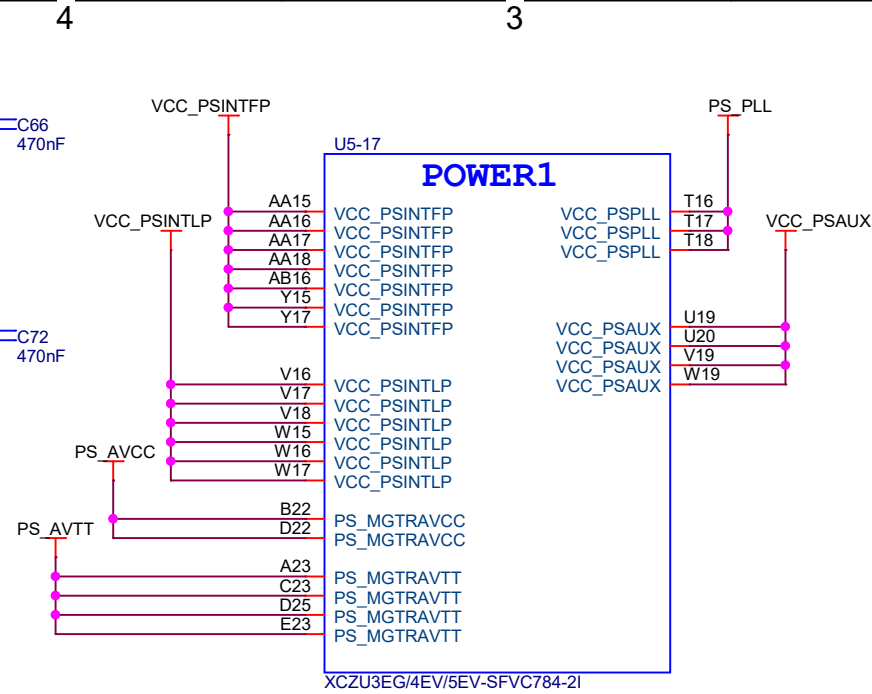
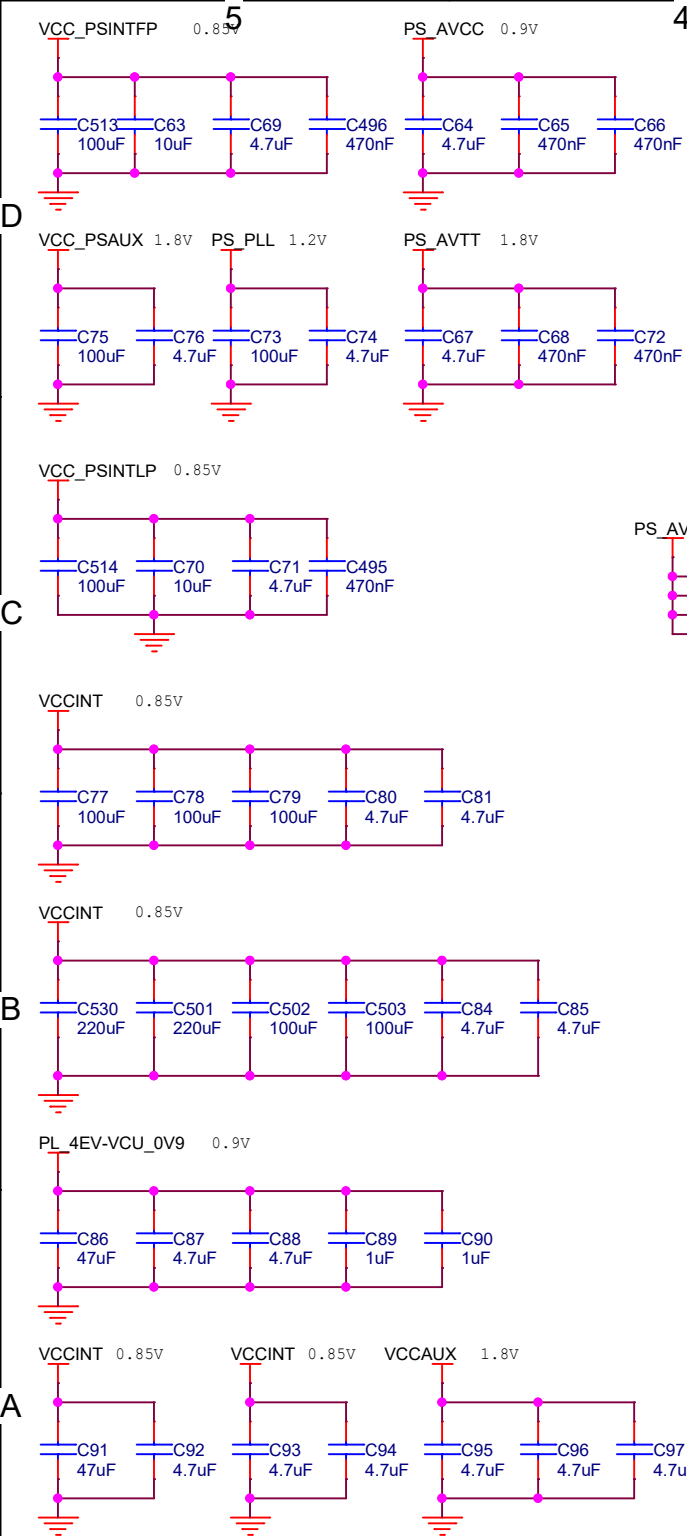


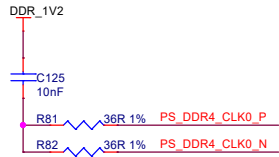
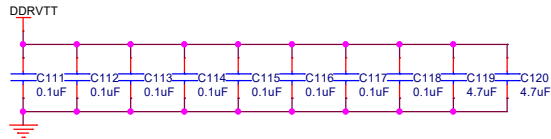
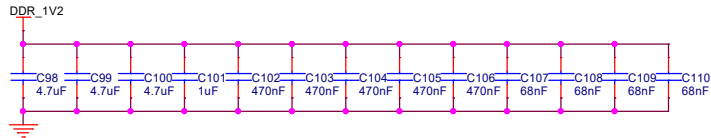
BANK65为HP(High Performance) BANK，应用于高性能也就是速度比较高的场景，由于速率比较高，Bank电压最高也只能到1.8V。



A







(7,14,15,16) PS_DDR4_A0
(7,14,15,16) PS_DDR4_A1
(7,14,15,16) PS_DDR4_A2
(7,14,15,16) PS_DDR4_A3
(7,14,15,16) PS_DDR4_A4
(7,14,15,16) PS_DDR4_A5
(7,14,15,16) PS_DDR4_A6
(7,14,15,16) PS_DDR4_A7
(7,14,15,16) PS_DDR4_A8
(7,14,15,16) PS_DDR4_A9
(7,14,15,16) PS_DDR4_A10
(7,14,15,16) PS_DDR4_A11
(7,14,15,16) PS_DDR4_A12
(7,14,15,16) PS_DDR4_A13

(7,14,15,16) PS_DDR4_BA0
(7,14,15,16) PS_DDR4_BA1
(7,14,15,16) PS_DDR4_BG0

(7,14,15,16) PS_DDR4_WE_B
(7,14,15,16) PS_DDR4_RAS_B
(7,14,15,16) PS_DDR4_CAS_B

(7,14,15,16) PS_DDR4_CLK0_P
(7,14,15,16) PS_DDR4_CLK0_N
(7,14,15,16) PS_DDR4_CKE0

(7,14,15,16) PS_DDR4_ACT_B
(7,14,15,16) PS_DDR4_ALERT_B
(7,14,15,16) PS_DDR4_PARITY

(7,14,15,16) PS_DDR4_RESET_B
(7,14,15,16) PS_DDR4_ODT0
(7,14,15,16) PS_DDR4_CS0_B

PS_DDR4_A0 P3
PS_DDR4_A1 P7
PS_DDR4_A2 R3
PS_DDR4_A3 N7
PS_DDR4_A4 A4
PS_DDR4_A5 P8
PS_DDR4_A6 P2
PS_DDR4_A7 P8
PS_DDR4_A8 R2
PS_DDR4_A9 R7
PS_DDR4_A10 M3
PS_DDR4_A11 T2
PS_DDR4_A12 M7
PS_DDR4_A13 T8

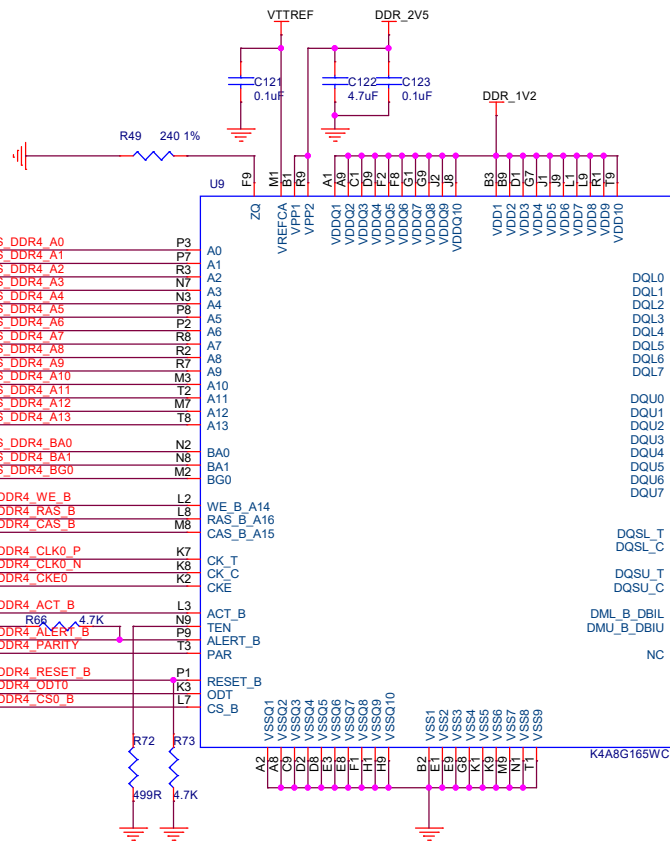
PS_DDR4_BA0 N2
PS_DDR4_BA1 BA0
PS_DDR4_BG0 M2

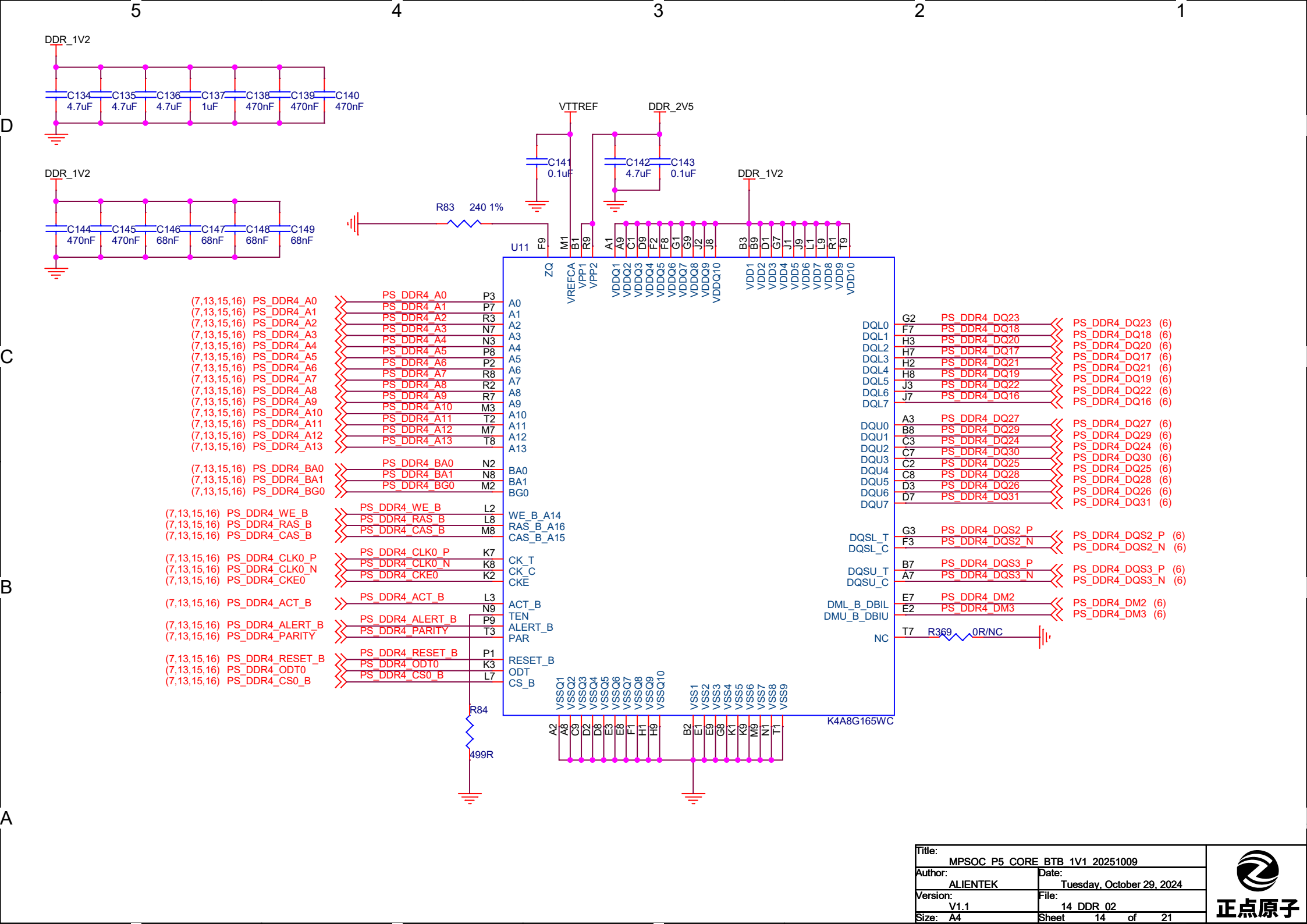
PS_DDR4_WE_B L2
PS_DDR4_RAS_B L8
PS_DDR4_CAS_B M8

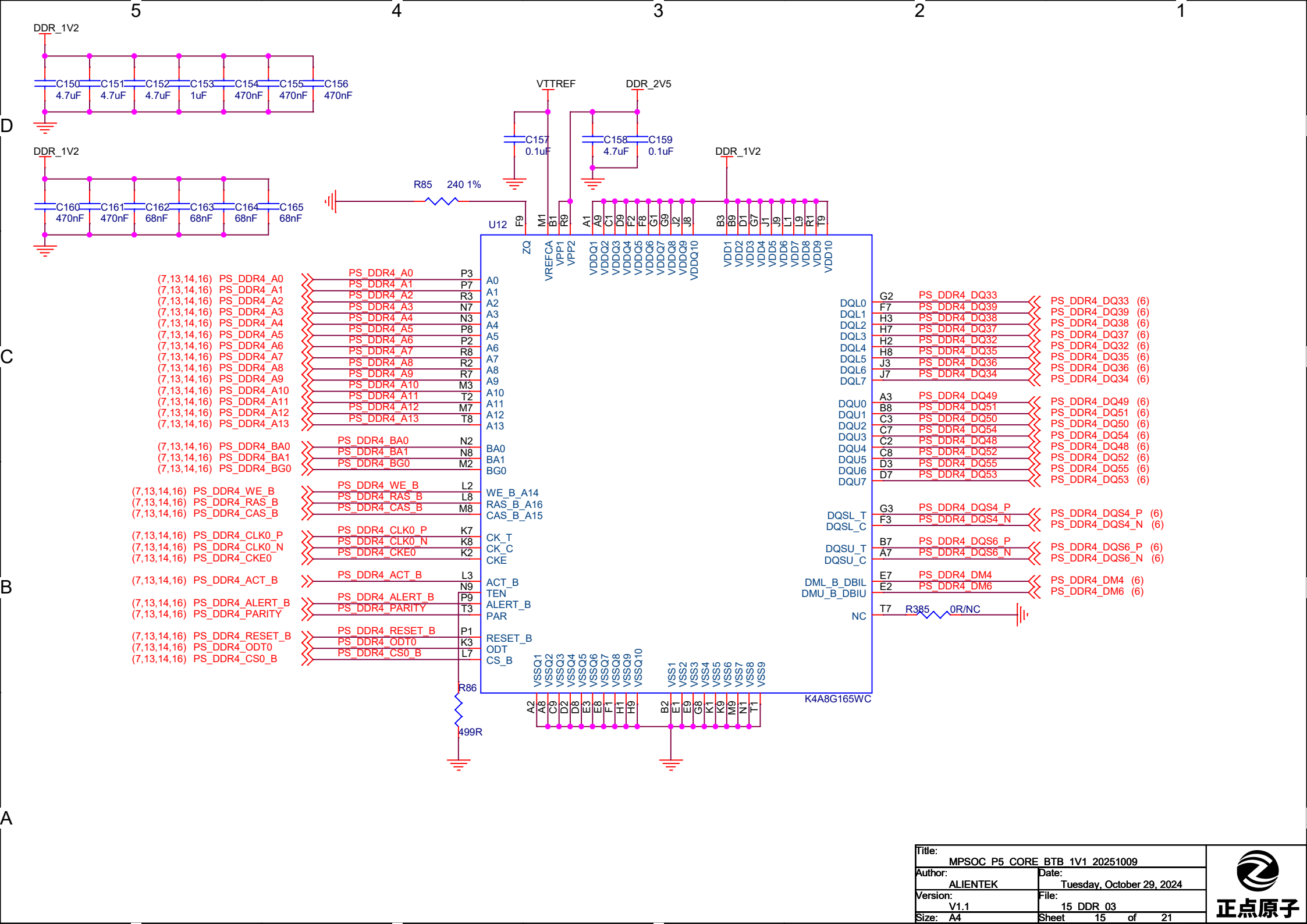
PS_DDR4_CLK0_P K7
PS_DDR4_CLK0_N K8
PS_DDR4_CKE0 K2

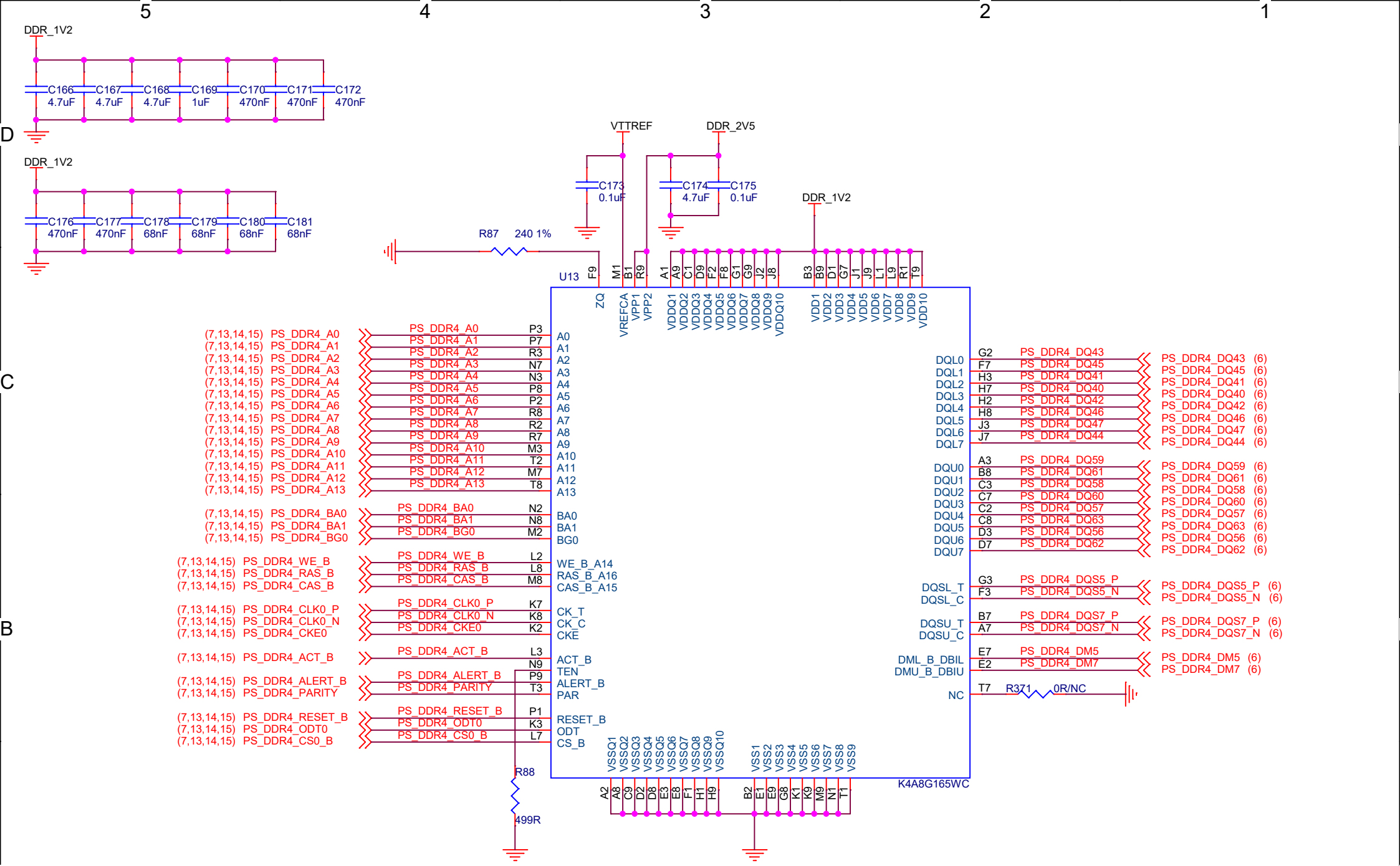
PS_DDR4_ACT_B L3
PS_DDR4_ALERT_B N9
PS_DDR4_PARITY T3

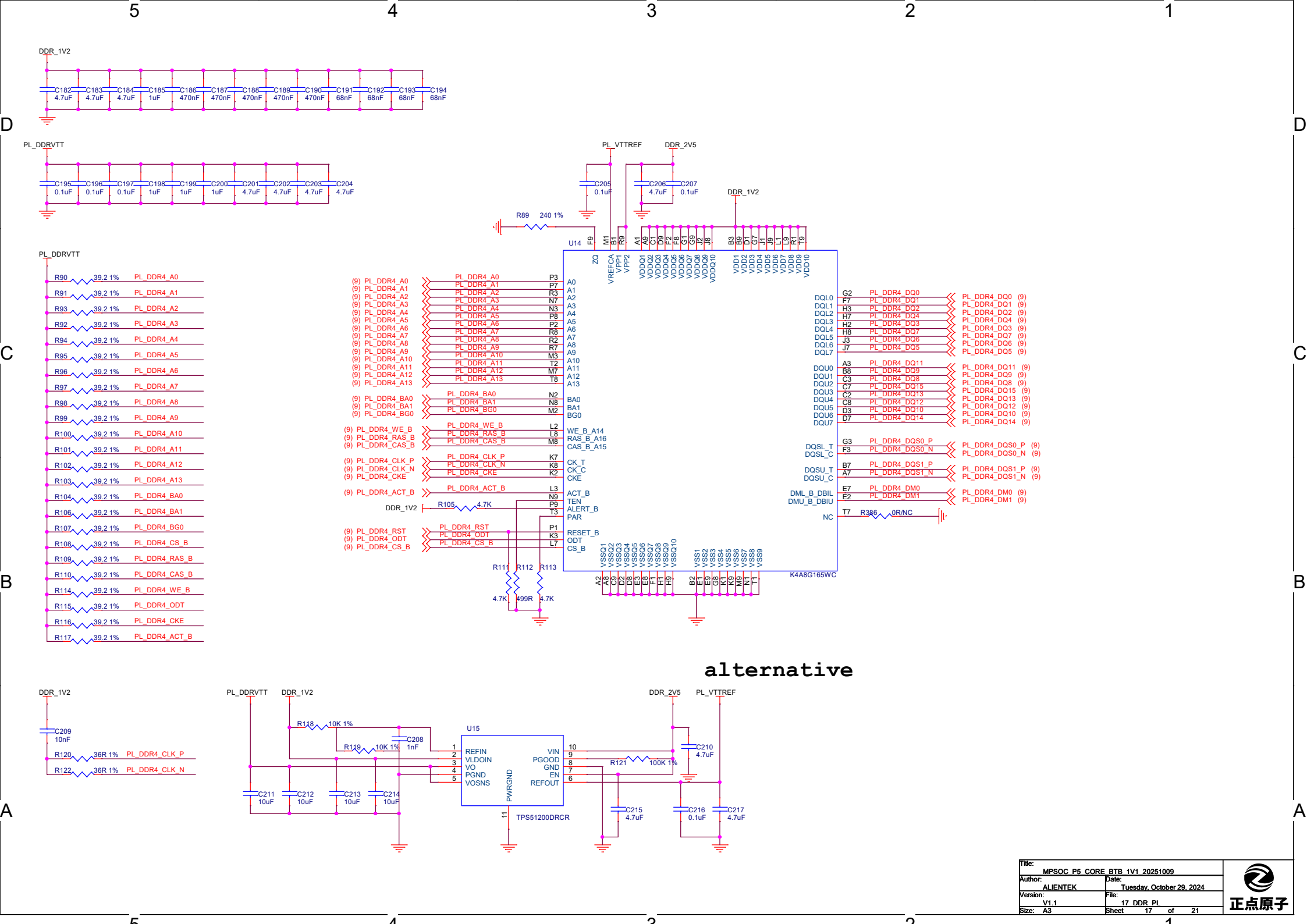
PS_DDR4_RESET_B P1
PS_DDR4_ODT0 K3
PS_DDR4_CS0_B L7

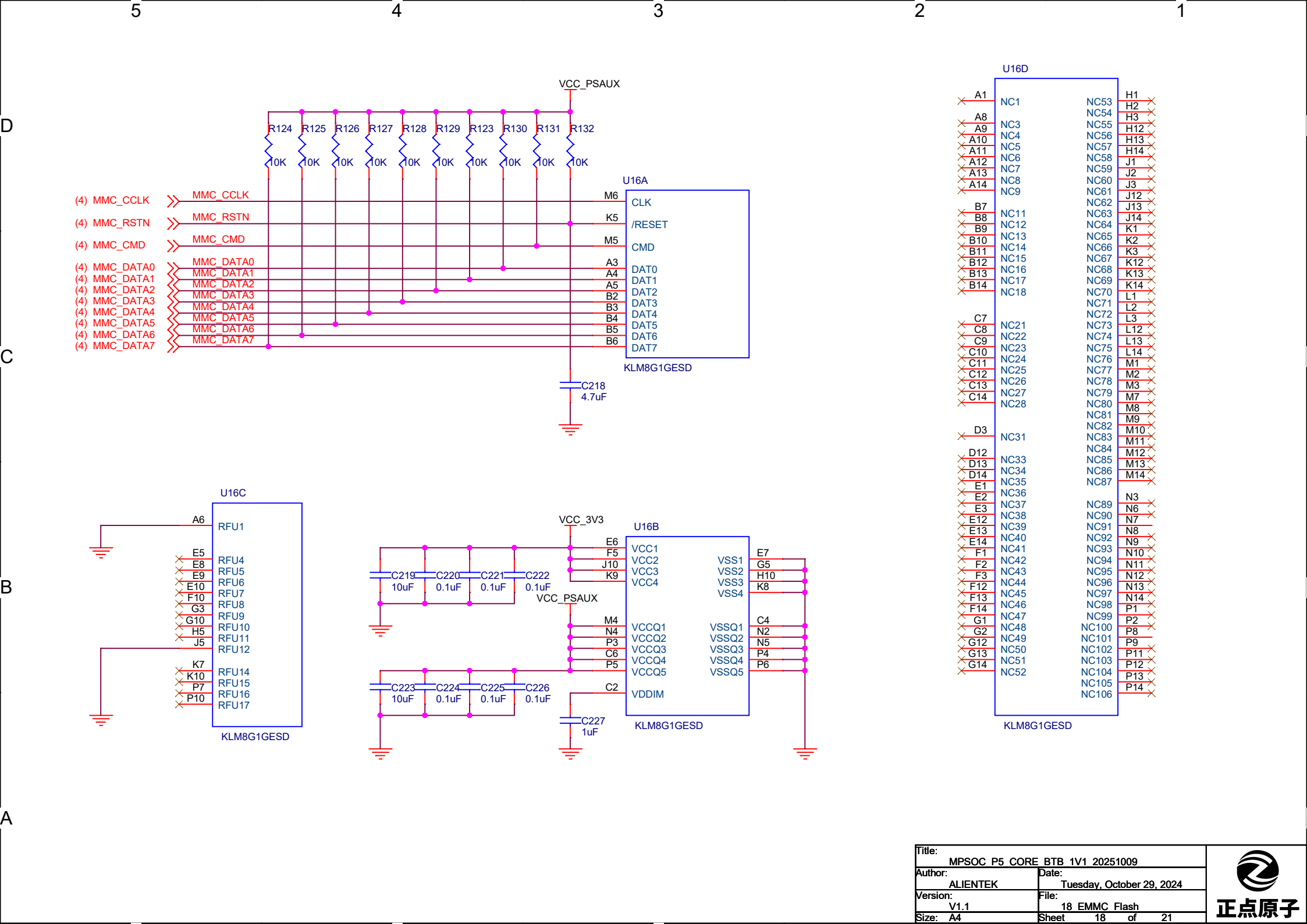




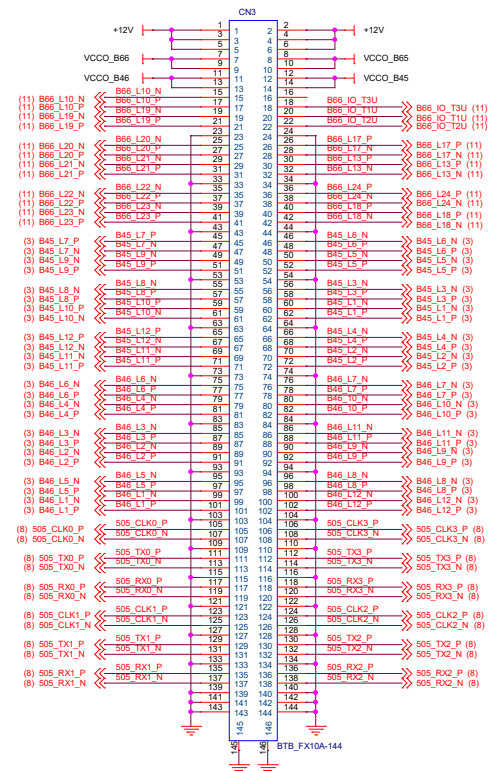
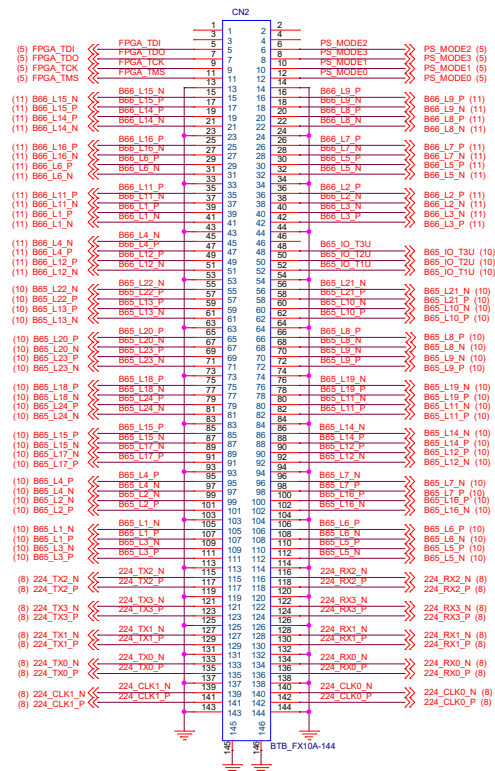
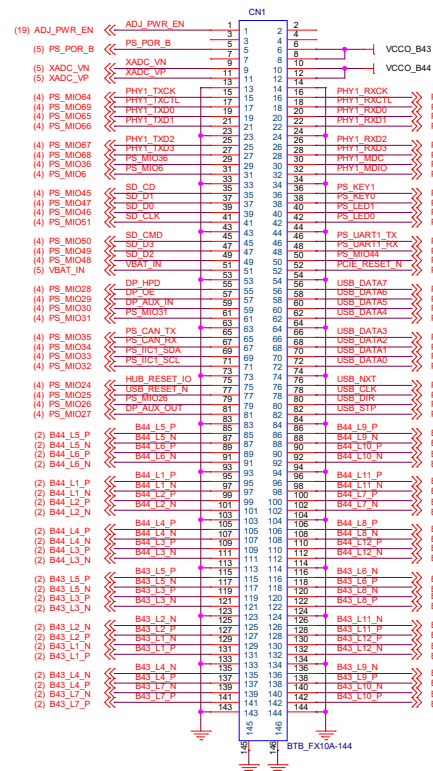










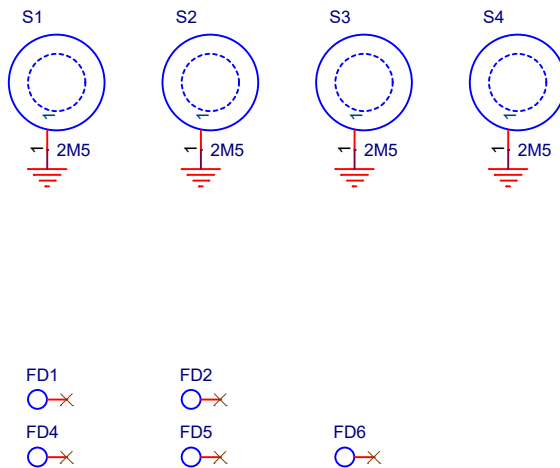


D

C

B

A



Title: MPSOC P5_CORE BTB 1V1 20251009	
Author: ALIENTEK	Date: Tuesday, October 29, 2024
Version: V1.1	File: 21 MTG_HOLE
Size: A4	Sheet 21 of 21

